

EE351 Course Syllabus for 2016

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Text: *Electronic Devices and Circuits*, (6th ed) Bogart, Beasley, Rico. The following is a general guide to class topics. Scheduling will adapt as appropriate. Labs are due the next lab session or as directed by the lab instructor.

Week	Lab	Topic	Chapter
S 1		Introduction and amplifier analysis	1,6
S 8		Semiconductor physics and diodes	2,3
S 15	1	Diode applications and power supplies	3,13
S 22	2	Bipolar transistor amplifier analysis	4,6,7
S 29		Bipolar transistor amplifier design	4,6,7
O 6	3	More BJT	4,6,7
O 13		Problem session – amplifiers, diodes, and BJTs	1,2,3,4,6,7
O 20		Test 1 and FET amplifier analysis	5,7
O 27	4	FET analysis and design	5,7
N 3		Multistage amplifiers	6,8
N 10	5	Multistage amplifiers	6,8
N 17	6	Frequency response	9
D 1	7	Test 2 (JFETs and multistage) , Monte Carlo analysis	lecture
D 8		Frequency response and conclusion	9
D 15		Final Exam (comprehensive)	

Grading:

Lab/assignments 20%
Average of Test 1 and Test 2 45%
Final (comprehensive) 35% Note: The final may count for more (and the two tests proportionately less) if the final scores are significantly better than the tests. Note: the lab must be passed in order to pass the course. Final grades are scaled as appropriate.

General Notes:

- Consider attendance mandatory. Lectures are detailed descriptions of how to approach and solve problems. Skipping class leads to a low final grade.
- Always bring your calculator and participate in class calculations.
- Exams are **closed**-book/notes except for one provided equation sheet. See Test Policies (including restrictions on calculators and other devices) on the web site.

There are seven laboratory experiments as described below.

Lab# Topic

1	Diode Circuits
2	Transistor Amplifier Biasing
3	Transistor Amplifier Circuits
4	Field Effect Transistors
5	Multistage Amplifiers
6	Frequency Response
7	Monte Carlo Experiment